Zhenhua Rd, Futian District, Shenzhen, China



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#### •REVISION RECORD

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0	20/10/07	INITIAL RELEASE	ALL	
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## 1. FEATURES

•Display construction ········· 16 Characters \* 2 Lines

•Display mode····· TN/STN

•Display type Positive Transflective

•Backlight······ LED(B/5.0V)
•Viewing direction····· 6 o' clock

•Operating temperature ····· Indoor

•Driving voltage ..... Single power

•Driving method··········· 1/16 duty, 1/5 bias •Type······· COB (Chip On Board)

•Number of data line ······ 8-bit parallel

•Connector ···· Pin

# 2. MECHANICAL DATA

	ITEM	WIDTH	HEIGHT	THICKNESS	UNIT
Modu	Module size		80. 0 36. 0		mm
View	Viewing area		27. 1	ı	mm
	Construction		5*7		dots
character	Size	2.95	4.35	ı	mm
	Pitch	3. 65	5. 05	ı	mm
D - 4	Size	0. 55	0.5	ı	mm
Dot	Pitch	0.6	0. 55	ı	mm
Diameter of	mounting hole	2. 7		mm	
W	eight	About 50		g	



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# 3. ABSOLUTE MAXIMUM RATINGS

Characteristics	Symbol	Ratings
Operating Voltage	VDD	-0.3V to +7.0V
Driver Supply Voltage	Vico	VDD-12V to VDD+0.3V
Input Voltage Range	Vin	-0.3V to VDD + 0.3V
Operating Temperature	Ta	0°C~+60°C
Storage Temperature	Tsto	-55℃~+125 ℃

## 4. ELECTRICAL CHARACTERISTICS

(TA = 25, VDD = 2.7 to 4.5V)

Ob annotonistics	C	Limit		1114	To at O and differen	
Characteristics	Symbol	Min.	Тур.	Max.	Unit	Test Condition
Operating Current	loo	-	0.2	0.4	mΑ	External clock (Note)
Input High Voltage	VIH1	0.7VDD	-	VDD	٧	Direct Do Day DDZ 40
Input Low Voltage	VIL1	-0.3	-	0.55	V	Pins:(E, RS, R/W, DB7 - 0)
Input High Voltage	VIH2	0.7VDD	-	VDD	٧	F:- 0004
Input Low Voltage	VIL2	-0.2	-	0.2VDD	V	Pin OSC1
Input High Current	Іш	-1.0	-	1.0	μА	Pins: (RS, R/W, DB7 - 0)
Input Low Current	lı∟	-5.0	-15	-30	μА	VDD = 3.0V
Output High		0.70/00			v	Iон = - 0.1mA
Voltage (TTL)	Voh1	0.75VDD	-	-	٧	Pins: DB7 - 0
Output Low	\/			0.2VDD	V	IoL = 0.1mA
Voltage (TTL)	Vol.1	-	-	0.2000		Pins: DB7 - 0
Output High	Voh2	0.8VDD		_	v	Іон = - 40μΑ,
Voltage (CMOS)	V OH2	0.8700	-	-	٧	Pins: CL1, CL2, M, D
Output Low	Vol2			0.2VDD	v	loι = 40μA, Pins:
Voltage (CMOS)	VOL2	-	-	0.2000	٧	CL1, CL2, M, D
Driver ON Resistance	D			20	VO.	Io = $\pm$ 50μA, VLco = 4V
(COM)	Rcom	-	-	20	ΚΩ	Pins: COM16 - 1
Driver ON Resistance	Rseg			30	ΚΩ	Io = $\pm 50 \mu$ A, VLCD = 4V
(SEG)	PASEG	-	-	30	F/2.2	Pins: SEG40 - 1
LCD Voltage	VLCD	3.0	-	11.0	V	VDD-V5, 1/4 bias or 1/5 bias



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## 5. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Contrast ratio	K	ф=0	1.4	4	-	_	1
Response time	Tr	ф=1	-	130	-	ms	2
(rise) Response time	Tf	ф=2		130	_	ms	2
(fall)	ф	V >1 /	10 +30 -30 +30		dom	2	
Viewing angle	θ	K ≥1.4			deg.	3	

Note 1: Definition of Contrast Ratio "K"

K= Brightness of non-selected segment(A)
Brightness of selected segment(B)

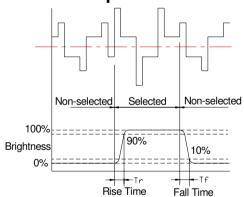
90%
Brightness Curve of selected area

Brightness Curve of non-selected area

10%

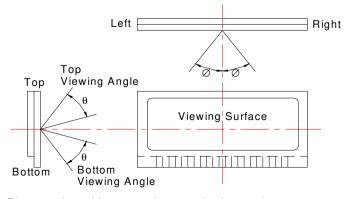
Vop

Note 2: Definition of Optical Response Time



**Note 3: Definition of Viewing Angle** 

Driving Voltage

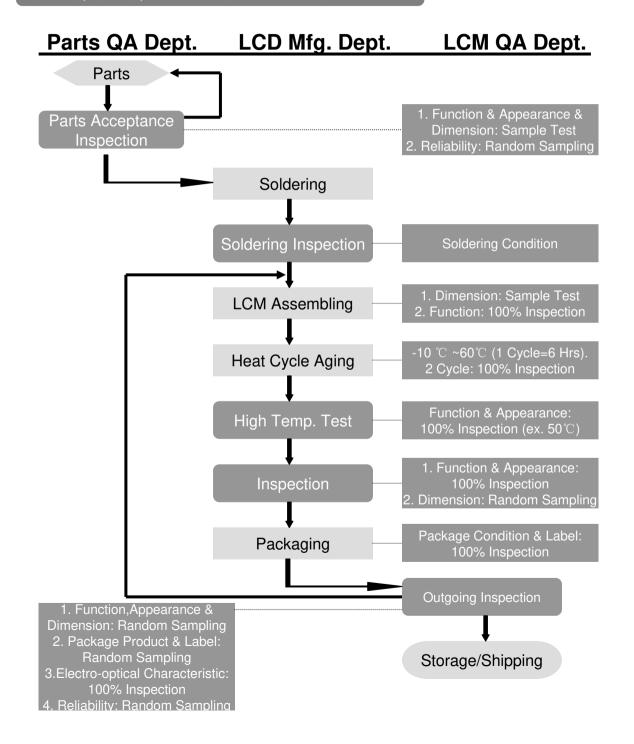


Please select either top or bottom viewing angle



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# 6. QC/QA PROCEDURE





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## 7. RELIABILITY

## •Operating life time:

Longer than 50000 hours (at room temperature without direct irradiation of sunlight)

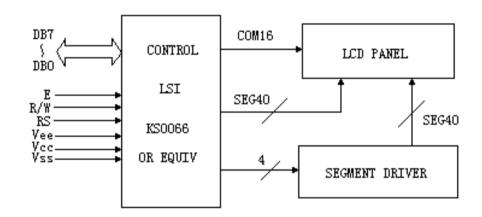
## •Reliability Characteristics:

Item	Test	Criterion
High temp	60℃ / 200 Hrs	STotal current
Low temp.	-10℃ / 200 Hrs	consumption should be below double of
High humidity	40℃ * 90%RH / 200 Hrs	initial value ⑤Contrast ratio
Thermal shock	-10°C→25°C→60°C→25°C /5 Cycles (30min) (5min) (30min) (5min)	should be within initial value±50%
Vibration	1. Operating time: Thirty minutes exposure in each direction (x, y, z) 2. Sweep Frequency (1min):10Hz→ 55Hz →10Hz 3. Amplitude: 0.75mm double amplitude	©No defect in cosmetic and operational function is allowable

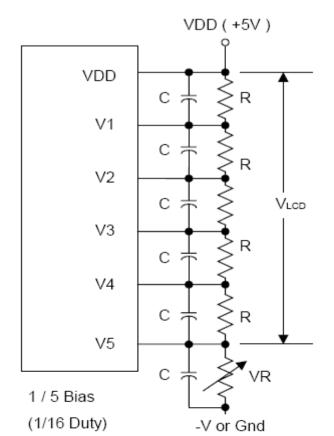


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# 8. BLOCK DIAGRAM



## 9. POWER SUPPLY

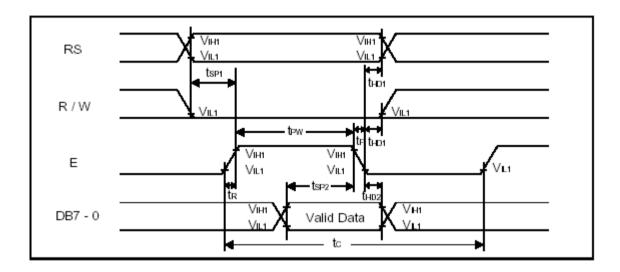




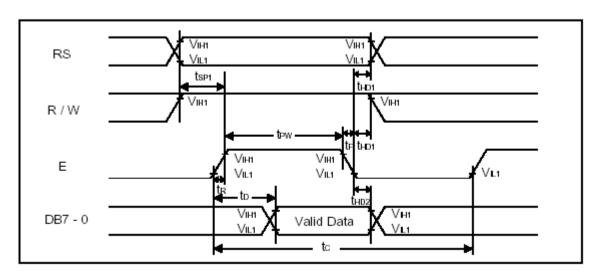
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# 10. TIMING DIAGRAM

#### • WRITE OPERATION



#### READ OPERATION





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# 11. AC CHARACTERISTICS

#### • WRITE MODE

			Limit			
Characteristics	Symbol	Min.	Тур.	Max.	Unit	Test Condition
E Cycle Time	tc	1000	-	-	ns	Pin E
E Pulse Width	tew	450	-	-	ns	Pin E
E Rise/Fall Time	tr, tr	-	-	25	ns	Pin E
Address Setup Time	tsp1	60	-	-	ns	Pins: RS, R/W, E
Address Hold Time	tho:	20	_	-	ns	Pins: RS, R/W, E
Data Setup Time	tsp2	195	-	-	ns	Pins: DB7 - 0
Data Hold Time	<b>t</b> HD2	10	-	_	ns	Pins: DB7 - 0

#### • READ MODE

			Limit			
Characteristics	Symbol	Min.	Тур.	Max.	Unit	Test Condition
E Cycle Time	tc	1000	-	-	ns	Pin E
E Pulse Width	tw	450	-	-	ns	Pin E
E Rise/Fall Time	tr, tr	,	-	25	ns	Pin E
Address Setup Time	tspi	60	-	-	ns	Pins: RS, R/W,E
Address Hold Time	tho1	20	-	-	ns	Pins: RS, R/W,E
Data Output Delay Time	to	,	-	360	ns	Pins: DB7 - 0
Data hold time	t <sub>HD2</sub>	5.0	-	-	ns	Pin DB7 - 0



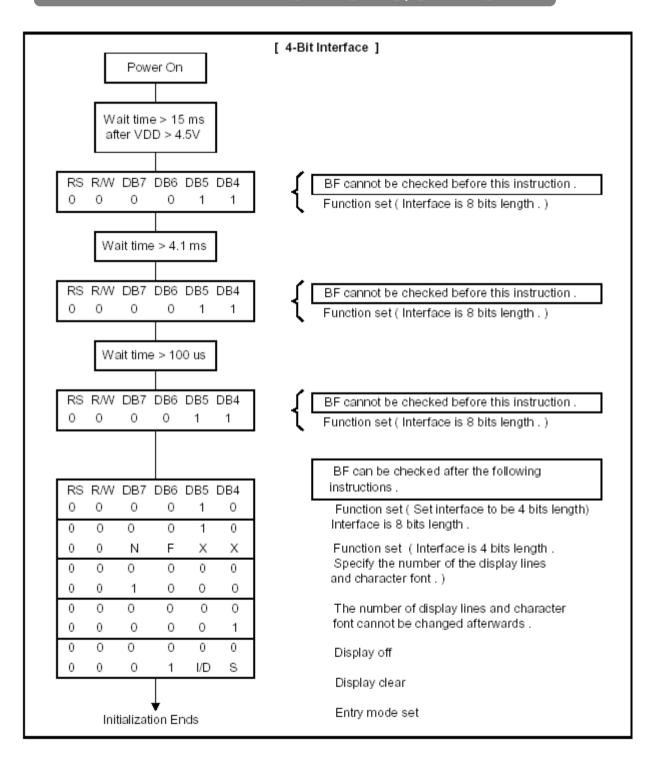
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# 12. INITIALIZATION SEQUENCE





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# 13. INSTRUCTION SET

COMMAND	COMMAND CODE COMMAND CODE				COMMAND CODE						COMMAND CODE	E-CYCLE
OOMINAND	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	OOMINAND OODL	f <sub>osc</sub> =250KHz
SCREEN CLEAR	0	0	0	0	0	0	0	0	0	1	Screen Clear, Set AC to 0 Cursor Reposition	1.64ms
CURSOR RETURN	0	0	0	0	0	0	0	0	1	*	DDRAM AD=0, Return, Content Changeless	1.64ms
INPUT SET	0	0	0	0	0	0	0	1	I/D	S	Set moving direction of cursor, Appoint if move	40us
DISPLAY SWITCH	0	0	0	0	0	0	1	D	С	В	Set display on/off,cursor on/off, blink on/off	40us
SHIFT	0	0	0	0	0	1	S/C	R/L	*	*	Remove cursor and whole display,DDRAM changeless	40us
FUNCTION SET	0	0	0	0	1	DL	N	F	*	*	Set DL,display line,font	40us
CGRAM AD SET	0	0	0	1			AC	CG			Set CGRAM AD, send receive data	40us
DDRAM AD SET	0	0	1				ADD				Set DDRAM AD, send receive data	40us
BUSY/AD READ CT	0	1	BF				AC				Executing internal function, reading AD of CT	40us
CGRAM/ DDRAM DATA WRITE	1	0			DATA WRITE						Write data from CGRAM or DDRAM	40us
CGRAM/ DDRAM DATA READ	1	1		DATA READ							Read data from CGRAM or DDRAM	40us
	S= S/ R/ DL N= F= BF	=1: Sh C=1: L=1: l _=1: 8 =1: 2F =1: 5x ==1: E	nift Displa Right SD D R N 10 Sty	ay Shi Shift; L=0: 4 =0: 11 yle; te Inte		C=0: 0 0: Lef 5x7 S Functi	Curso t Shift Style	r Shif		de	DDRAM: Display data RAM CGRAM: Character Generator RAM ACG: CGRAM AD ADD: DDRAM AD & Cursor AD AC: Address counter for DDRAM & CGRAM	E-cycle changing with main frequency. Example: If fcp or fosc=270KHz 40us x 250/270 =37us



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# 14. FONT TABLE

0000 RAM	
0000 RAM (1) 0001 (2) ! I F D S S S S S S S S S S S S S S S S S S	9 8 8
0010 (3) "2BRDF 「1"	6 8 9
0011 (4) # 3 C S C S J D T E S 1 0100 (5) \$ 4 D T C t T F 17 J S 1 0101 (6) % 5 E U e U - 7 7 1 C	8 8 9
0011 (4) # .5 L. 5 C. 5 J	
	•••
	_
0110 (7) <u>8.6 F U f v 7 J 3 p</u>	
	ŢŢ
	×
1010 (3) * # JZ j z = 1 11 L j	<del>-</del>
1011 (4) + 3 K C k ( 7	Fi
1100 (5) <b>3 &lt; L ¥ 1 1 1 1 5 5 7 7 4</b> 1	P
1101 (6) III M J M J A Z ^ L	
1110 (7) > M ^ m + 3 t	
1111 CG RAM	



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## 15. Handling Precautions

#### 1. Limitation of Application:

Optrex products are designed for use in ordinary electronic devices such as business machines, telecommunications equipment measurement devices and etc. Please handle the products with care. (see below)

Optrex products are not designed,intended ,or authorized for use in any application which the failure of the product could result in a situation where personal injury or death may occur . these applications include, but are not limited to . life-sustaining equipment,nuclear control devices , aerospace equipment , devices related to hazardous or flammable materials , etc.[If Buyer intends to purchase or use the Optrex Products for such unintended or unauthorized applications , Buyer must secure prior written consent to such use by a responsible officer of Optrex Corporation.]Should Buyer purchase or use Optrex Products for any such unintended or unauthorized application [ without such consent ].Buyer shall indemnify and hold Optrex and its officers. employees. subsidiaries, affiliates and distributors harmless against all claims, costs, damages and expenses , and reasonable attorney's fees, arising out of , directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Optrex was negligent regarding the design or manufacture of the part. 2.Industrial Rights and Patents

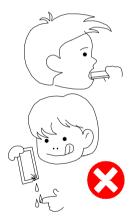
Optrex shall not be responsible for any infringement of industrial property rights of third parties in any country arising out of the application or use of Optrex products, except which directly concern the structure or production of such products.

#### No Press and Shock!

# If pressure to LCD, orientation may be disturbed. LCD will broken by shock!

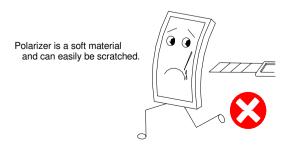
#### Don't Swallow or Touch Liquid Crystal!

Liquid Crystal may be leaked when display is broked. If it accidentally gets your hands, wash then with water!



#### Don't not Scratch!

#### No DC Voltage to LCD!



DC volrage or driveing higher than the specified voltage will reduce the lifetime of the LCD







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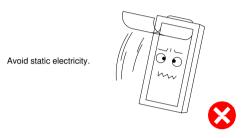
# **Don't Press the Metallic Frame and Disassemble the LCM**

#### **Slowly Peel Off Protective Film!**

Pressure on the metallic frame and PCB may deform the conductive rubber or break the liquid crystal cell and back light, which will cause defects.

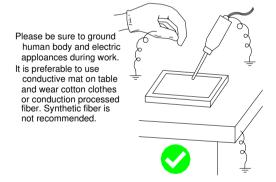
LCD may be shifted or conductive rubber may be reshaped, which will cause defects.

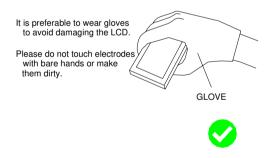




#### **Avoid Static Electricity!**

#### **Wear Gloves While Handing!**





#### **Keep Away From Extreme Heat and Humidity!**

#### **Use Alcohol to Clean Terminals!**

LCD deteriorates.



When attaching with the heat seal or anisontropically conductive film, wipe off with alcohol before use.





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#### **Don't Drop Water on LCD!**

Note that the presence of waterdrops or dew in the LCD panel may deteriorate the polarizer or corrade electrode.



#### **Precaution in Soldering LCD Module**

Basic instructions: Solder I/O terminals only.

Use soldering iron without leakage.

(1) Soldering condition to I/O terminals

Temperature at tip of the iron:  $280 \pm 10^{\circ}$ C

Soldering time: 3~4 sec.

Type of solder: Eutectic solder (containing colophony-flux)

- \*Please do not use flux because it may soak into LCD Module or contaminate it.
- \*It is preferable to peel off protective film on display surface after soldering I/O terminals is finished.
- (2)Remove connector or cable
  - \*When you remove connector or cable soldered to I/O terminals, please confirm that solder is fully melted. If you remove by force, electrodes at I/O terminals may be damaged(or stripped off).
  - \*It is recommended to use solder suction machine.

#### Long-term Storage

If it is necessary to store LCD modules for a long time, please comply with the following procedures.

If storage condition is not satisfactory, display(especially polarizer) may be deteriorated or soldering I/O terminals may become difficult(some oxide is generated at I/O terminals plating).

- 1.Store as delivered by Optrex
- 2.If you store as unpacked,put in anti-static bag,seal its opening and store where it is not subjected to direct sunshine nor fluorescent lamp.
- 3. Store at temperature 0 to +35°C and at low humidity. Please refer to our specification sheets for storage temperature range and humidity condition.

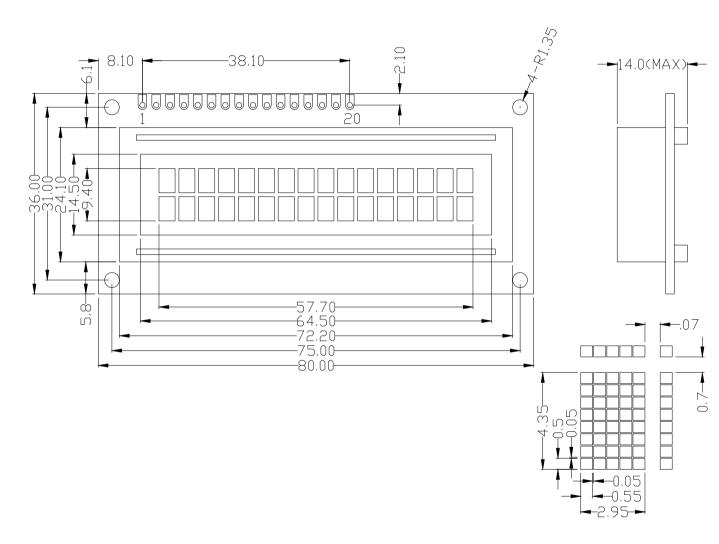
#### Long-term Storage

Please use power supply with built-in surge protection circuit.



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# 16. EXTERNAL DIMENSION



1	2	3	4	5	6	7	8
VSS	VCC	VEE	RS	R/W	E	DB0	DB1
9	10	11	12	13	14	15	16
DB2	DB3	DB4	DB5	DB6	DB7	LED+	LED-



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# 17. INTERFACE

PIN NO.	SYMBOL	DESCRIPTION	FUNCTION
1	VSS	GROUND	0V (GND)
2	VCC	POWER SUPPLY FOR LOGIC CIRCUIT	+5V
3	VEE	LCD CONTRAST ADJUSTMENT	
4	RS	INSTRUCTION/DATA REGISTER SELECTION	RS = 0 : INSTRUCTION REGISTER RS = 1 : DATA REGISTER
5	R/W	READ/WRITE SELECTION	R/W = 0 : REGISTER WRITE R/W = 1 : REGISTER READ
6	Е	ENABLE SIGNAL	
7	DB0		
8	DB1		
9	DB2		
10	DB3	DATA INPUT/OUTPUT LINES	8 BIT: DB0-DB7
11	DB4		8 BH. DB0-DB7
12	DB5		
13	DB6		
14	DB7		
15	LED+	SUPPLY VOLTAGE FOR LED+	+5V
16	LED-	SUPPLY VOLTAGE FOR LED-	0V